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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

ELIYAHOU HARARI, ROBERT D.
NORMAN and SANJAY MEHROTRA

Serial No.: 08/789,421

Filed: January 29, 1997

For: FLASH EEPROM SYSTEM

Group Art Unit: ~~102~~ /B

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Worth!
10/30/97

San Francisco, California

Hon. Commissioner of
Patents and Trademarks
Washington, D.C. 20231

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Sir:

Please amend the claims of the above-identified patent application prior to a substantive examination thereof, as follows:

Cancel claims 56-62, without prejudice, all the claims presently in the present application, and substitute the following new claims therefor:

Sub C3
--63. A method of operating with a host processor a bulk storage memory including an array of non-volatile floating gate memory cells, comprising:

storing, within defined non-overlapping groups of array user data and associated overhead data containing information of individual memory array cell groups,

B
in response to receipt from the host processor of an address in a format designating at least one mass memory storage block address, converting said at least one mass memory storage block address into an address of at least one of the memory array cell groups,

in response to receipt from the host processor of a command to write user data to said at least one mass memory storage block address, writing at least one sector of said user data and

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